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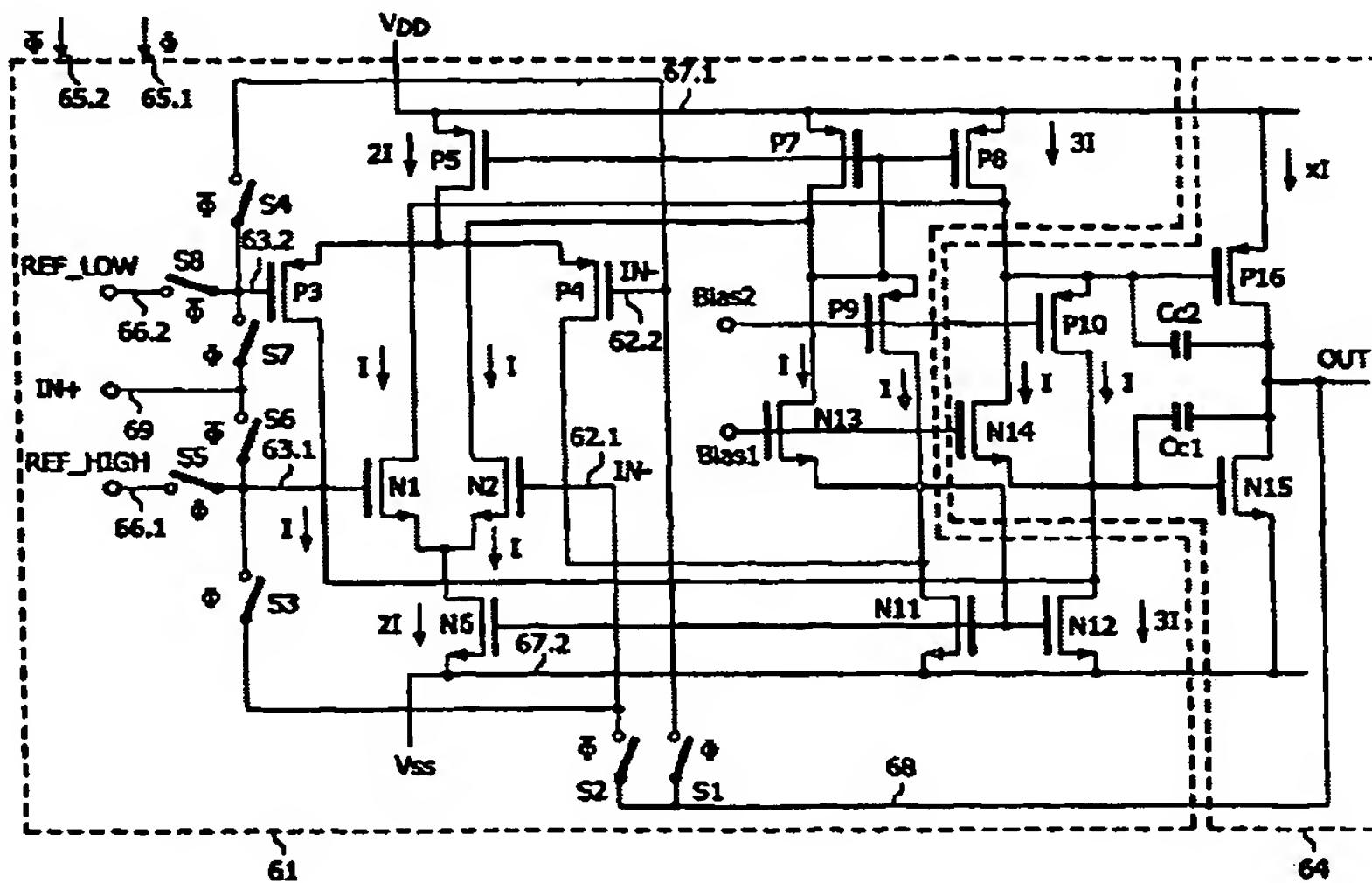
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(54) Title: OPERATIONAL AMPLIFIER WITH CONSTANT OFFSET AND APPARATUS COMPRISING SUCH AN OPERATIONAL AMPLIFIER



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(57) Abstract: Apparatus (80) comprising an input stage (61) with an NMOS transistor doublet having a first differential input for receiving input signals, and a PMOS transistor doublet having a second differential input for receiving input signals. The apparatus (80) further comprises switching means for receiving and selectively directing analog input signals either to the first differential input or to the second differential input. The means are controlled by a switching signal ($\phi, \bar{\phi}$) in a manner to keep the ratio of the transconductance of the NMOS transistor doublet and the transconductance of the PMOS transistor doublet constant.